

FIG. 2

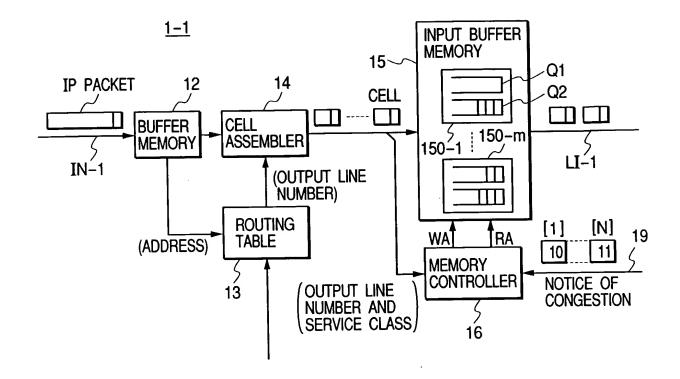
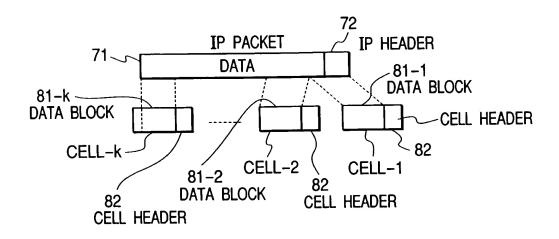
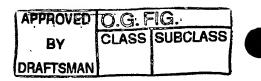


FIG. 3





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FIG. 4a

FIG. 4b

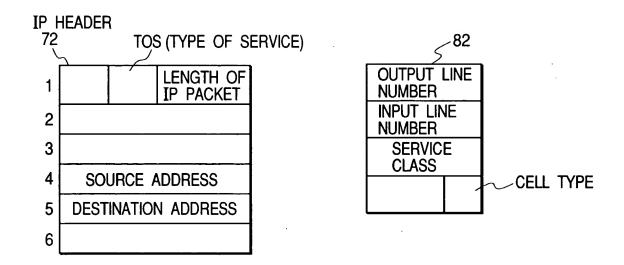


FIG. 5 <u>3</u> 32 - 1**CELL TRANSFER BUFFER MEMORY** CONTROLLER LI-1323 LO-1 **ADDRESS FILTER** 324 READ 321 CONTROLLER MULTIPLEXOR L-n-**BUFFERING** 322-**MONITOR** CLK-1 32-n LI-n LO-n CELL TRANSFER CONTROLLER CLK-n

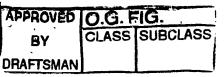
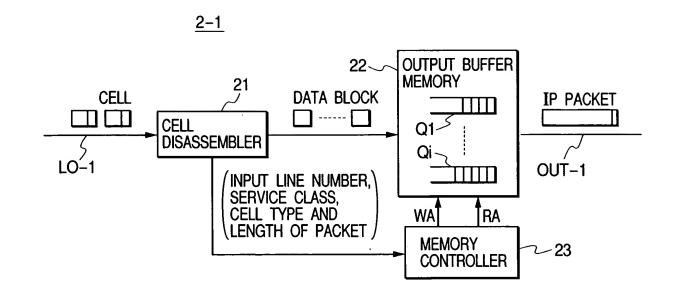


FIG. 6

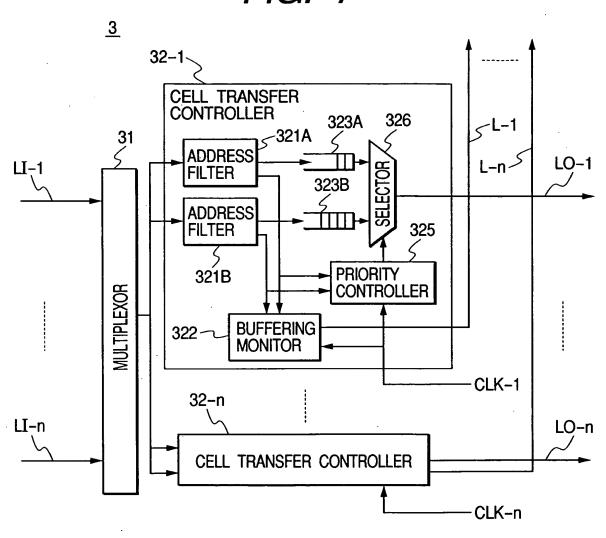


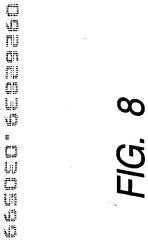


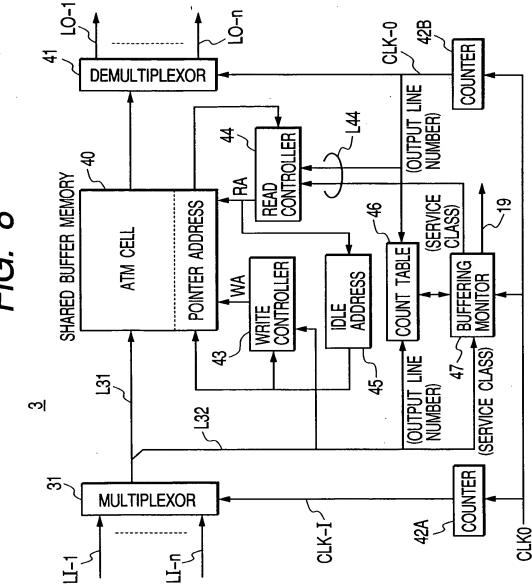
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FIG. 7







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FIG. 9

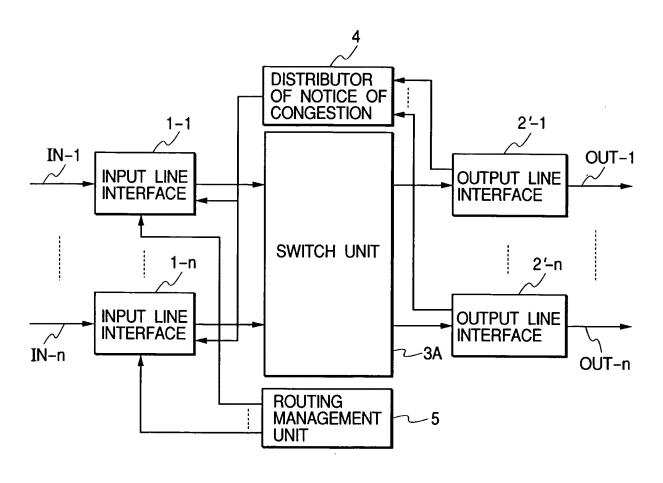
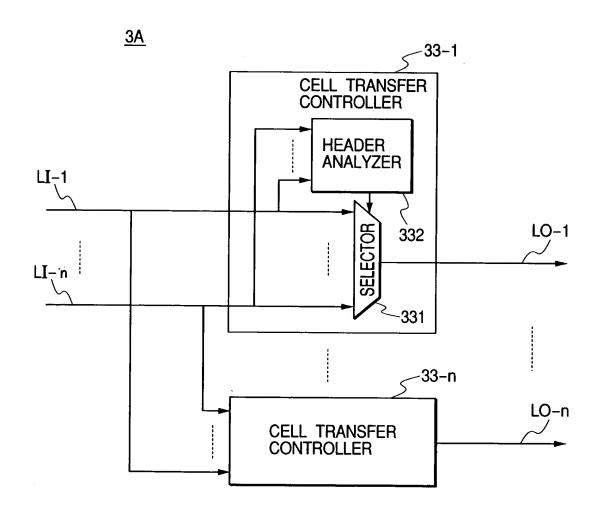


FIG. 10



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FIG. 11

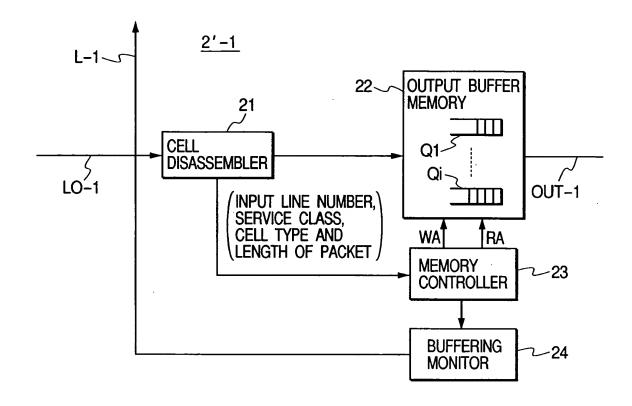


FIG. 12

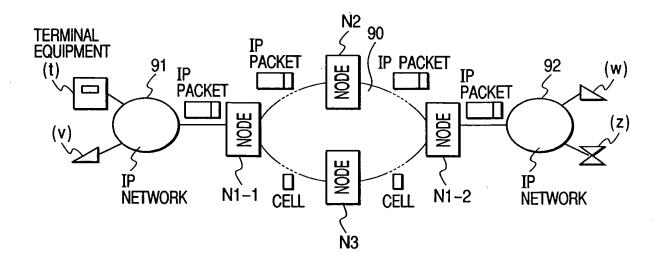
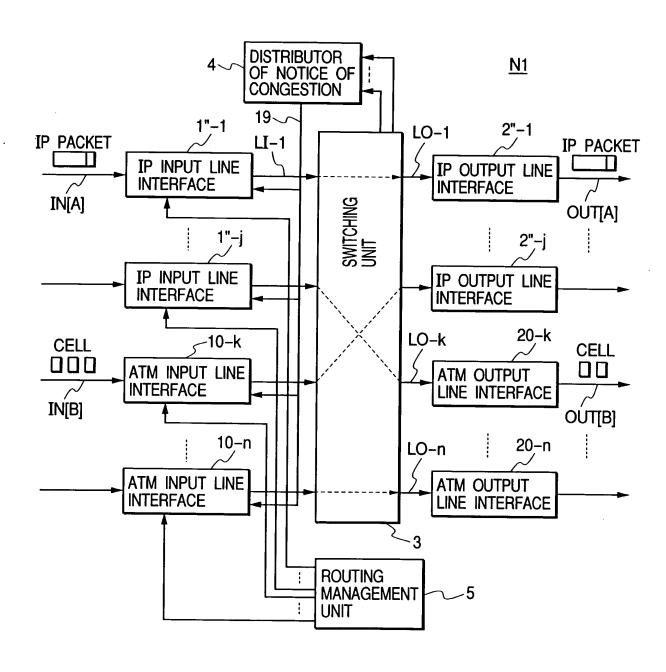


FIG. 13



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FIG. 14a

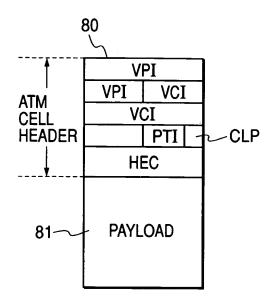


FIG. 14b

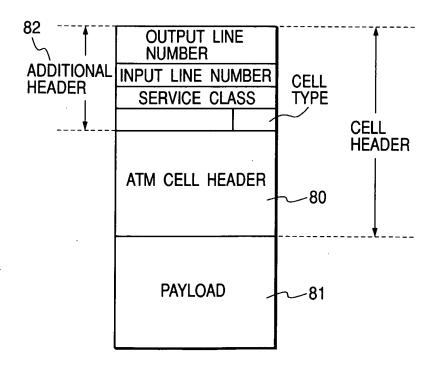


FIG. 15

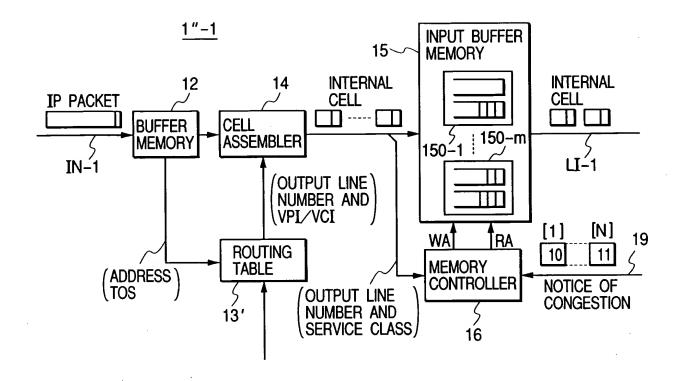
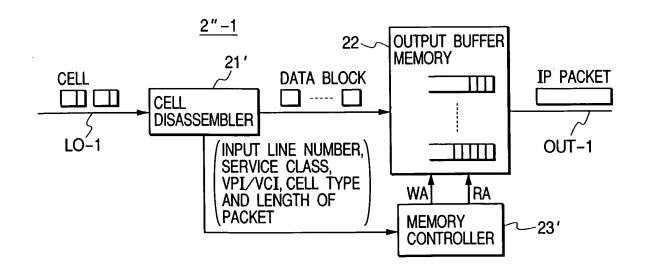


FIG. 16



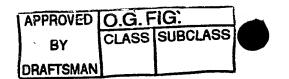


FIG. 17

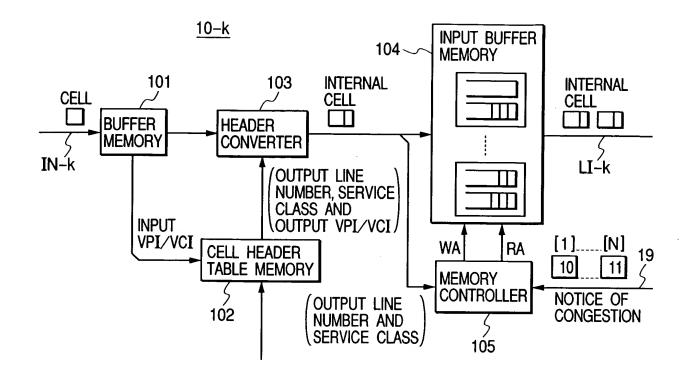


FIG. 18

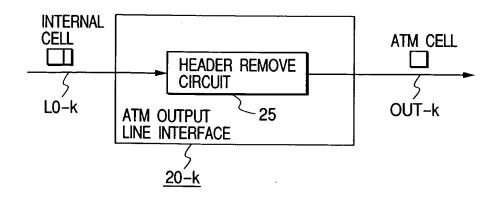
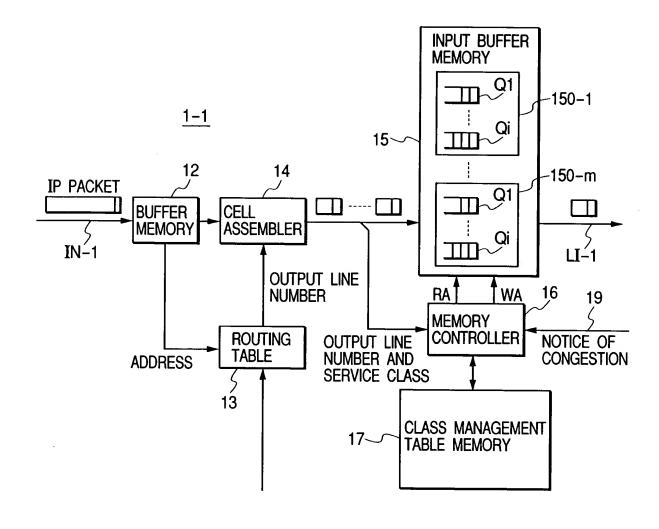


FIG. 19

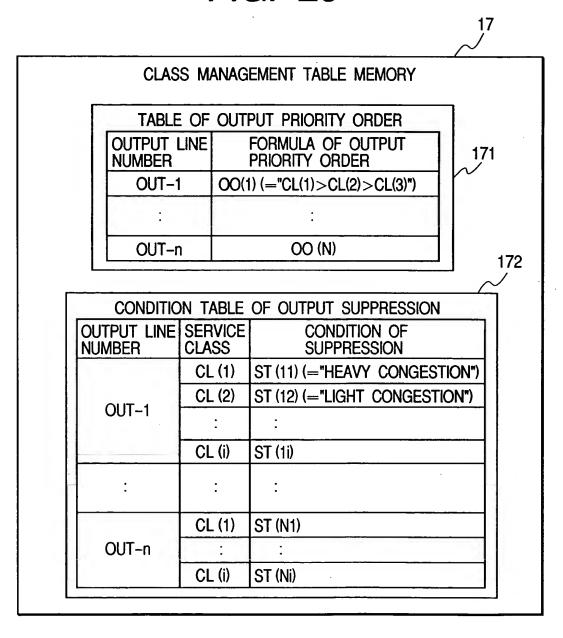


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FIG. 20



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